

U.S. Patent Application Serial No. 09/806,054

*PL*  
*BN*  
this case, the signal of the frequency  $f_{CK}$  may be converted into a voltage signal by a frequency/voltage converter, and this voltage signal compared with a predetermined value by a voltage comparator.

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**IN THE CLAIMS:**

Cancel claim 3.

Amend claim 1 as follows:

*H2*  
1. (Amended) A PLL circuit in which a phase comparator, a loop filter, a voltage control oscillator and a frequency divider are successively loop-connected, said PLL circuit comprising:

*H2*  
operation stoppage detecting means for detecting that PLL operation has stopped, said detection being effected on the basis of an output signal from said voltage control oscillator or said frequency divider; and

*2nd*  
*B,*  
control means for, when said operation stoppage detecting means detects stoppage of operation, controlling the voltage control oscillator such that an oscillation frequency of the voltage control oscillator is low.

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